



# 3HVRC SERIES

3 to 75kV, 1.75A, Standard Recovery Rectifier Blocks with Insert Mounting



## Features

- Medium Power
- Single and 2-Leg Versions, Resistor Capacitor Compensated
- Epoxy Molded Encapsulation with Mounting Inserts
- Threaded Brass Solder Terminal Inserts

## Specifications<sup>1</sup>

Part Number <sup>2</sup>	$V_{RRM}^3$ V	$I_{FAVM}^3$ A	$V_F^3$ V	$I_R$ $\mu A$	$I_{FSM}$ A	L in.
<b>Common Anode (N) Center Tap Subseries (Figure 1)</b>						
3HVN5RC	5000	1.75	4.8	500	150	4.50
3HVN8RC	8000	1.75	7.5	500	150	5.50
3HVN10RC	10000	1.75	9.0	500	150	7.00
3HVN12RC	12000	1.75	10.8	500	150	8.25
3HVN15RC	15000	1.75	14.0	500	150	10.25
3HVN20RC	20000	1.75	18.0	500	150	13.25
3HVN25RC	25000	1.75	23.0	500	150	16.25
<b>Common Cathode (P) Center Tap Subseries (Figure 2)</b>						
3HVP5RC	5000	1.75	4.8	500	150	4.50
3HVP8RC	8000	1.75	7.5	500	150	5.50
3HVP10RC	10000	1.75	9.0	500	150	7.00
3HVP12RC	12000	1.75	10.8	500	150	8.25
3HVP15RC	15000	1.75	14.0	500	150	10.25
3HVP20RC	20000	1.75	18.0	500	150	13.25
3HVP25RC	25000	1.75	23.0	500	150	16.25
<b>Doubler-Style Center Tap Subseries (Figure 3)</b>						
3HVD5RC	5000	1.75	4.8	500	150	4.50
3HVD8RC	8000	1.75	7.5	500	150	5.50
3HVD10RC	10000	1.75	9.0	500	150	7.00
3HVD12RC	12000	1.75	10.8	500	150	8.25
3HVD15RC	15000	1.75	14.0	500	150	10.25
3HVD20RC	20000	1.75	18.0	500	150	13.25
3HVD25RC	25000	1.75	23.0	500	150	16.25
<b>Non-Tapped Subseries (Figure 4)</b>						
3HV3RC	3000	1.75	2.4	500	150	2.38
3HV5RC	5000	1.75	4.8	500	150	2.38
3HV8RC	8000	1.75	7.5	500	150	2.38
3HV10RC	10000	1.75	9.0	500	150	4.50
3HV12RC	12000	1.75	10.8	500	150	4.50
3HV15RC	15000	1.75	14.0	500	150	5.50
3HV20RC	20000	1.75	18.0	500	150	7.00
3HV25RC	25000	1.75	22.0	500	150	8.25
3HV30RC	30000	1.75	28.0	500	150	10.25
3HV40RC	40000	1.75	36.0	500	150	13.25
3HV50RC	50000	1.75	44.0	500	150	16.25
3HV75RC	75000	1.75	69.0	500	150	25.00



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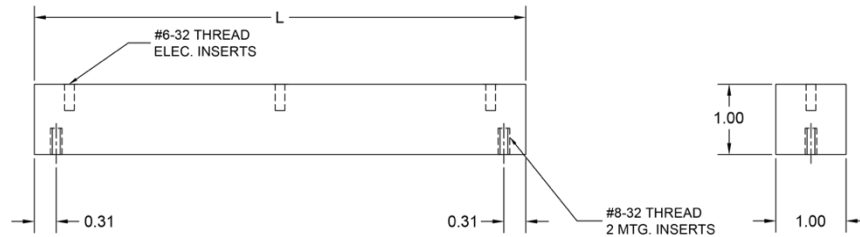
Temperature °C	
Operating Temperature	-55 to 150
Storage Temperature	-55 to 150
Maximum Junction Temperature	150

<sup>1</sup>25°C ambient temperature unless stated otherwise.

<sup>2</sup>For solder turrets add suffix "T"; for mounting studs add suffix "S".

<sup>3</sup>Check Specification Definitions for conditions details.

## Drawings

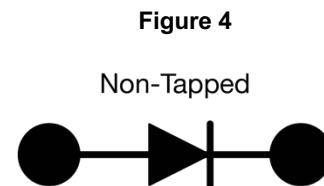
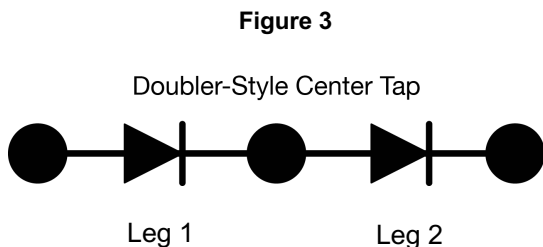
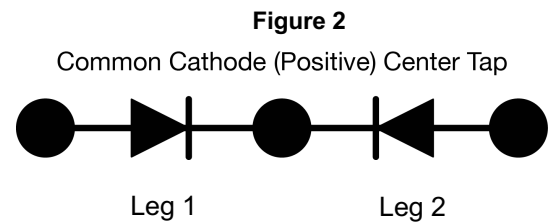
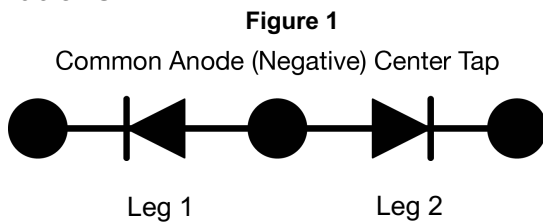


Dimensions in inches, tolerances  $\pm 0.020$  except as noted

## Specification Definitions

Specifications		Conditions
$V_{RRM}$	Maximum Repetitive Reverse Voltage	Per Leg
$I_{FAVM}$	Maximum Average Forward Current	At $T_A = 55^\circ\text{C}$ , Per Leg
$V_F$	Maximum Forward Voltage Drop	At $I_{FAVM}$ , Per Leg
$I_R$	Maximum Leakage Current	At $V_{RRM}$
$I_{FSM}$	Maximum Surge Current	At 8.3 mS, Single Half Sine

## Configurations:



Note: Specifications subject to change without notice. Photo is representation only.

